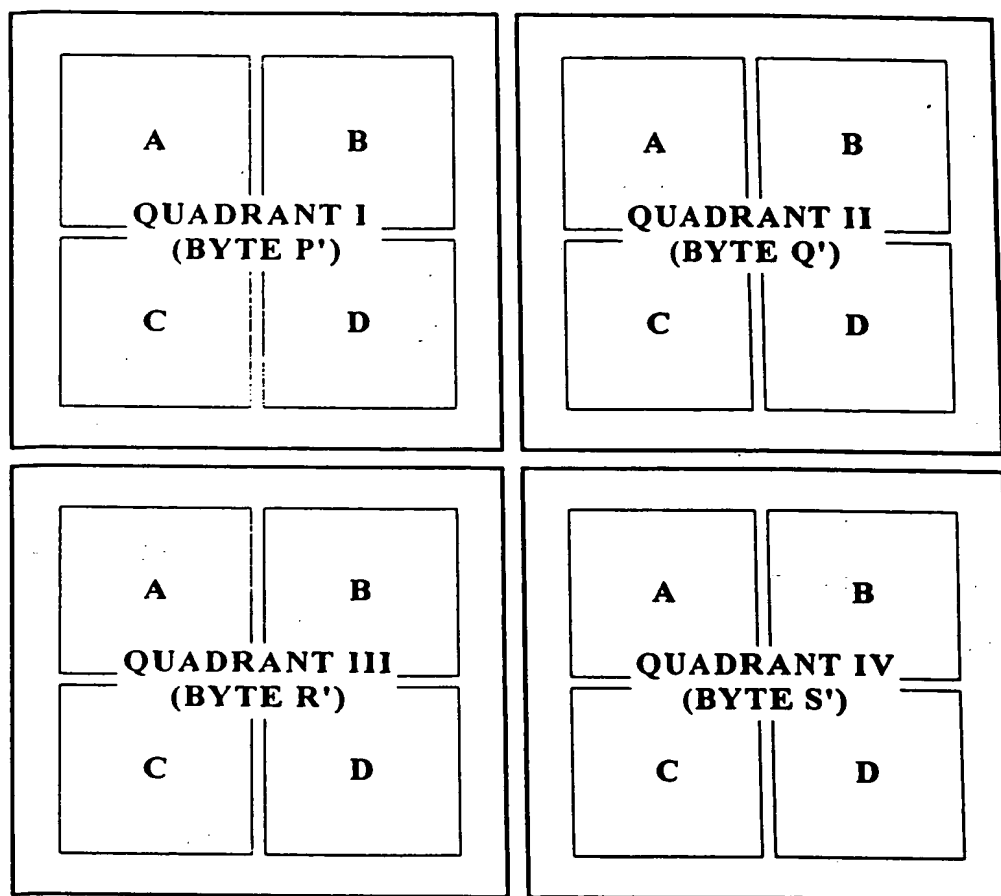


400 ppi pixel E

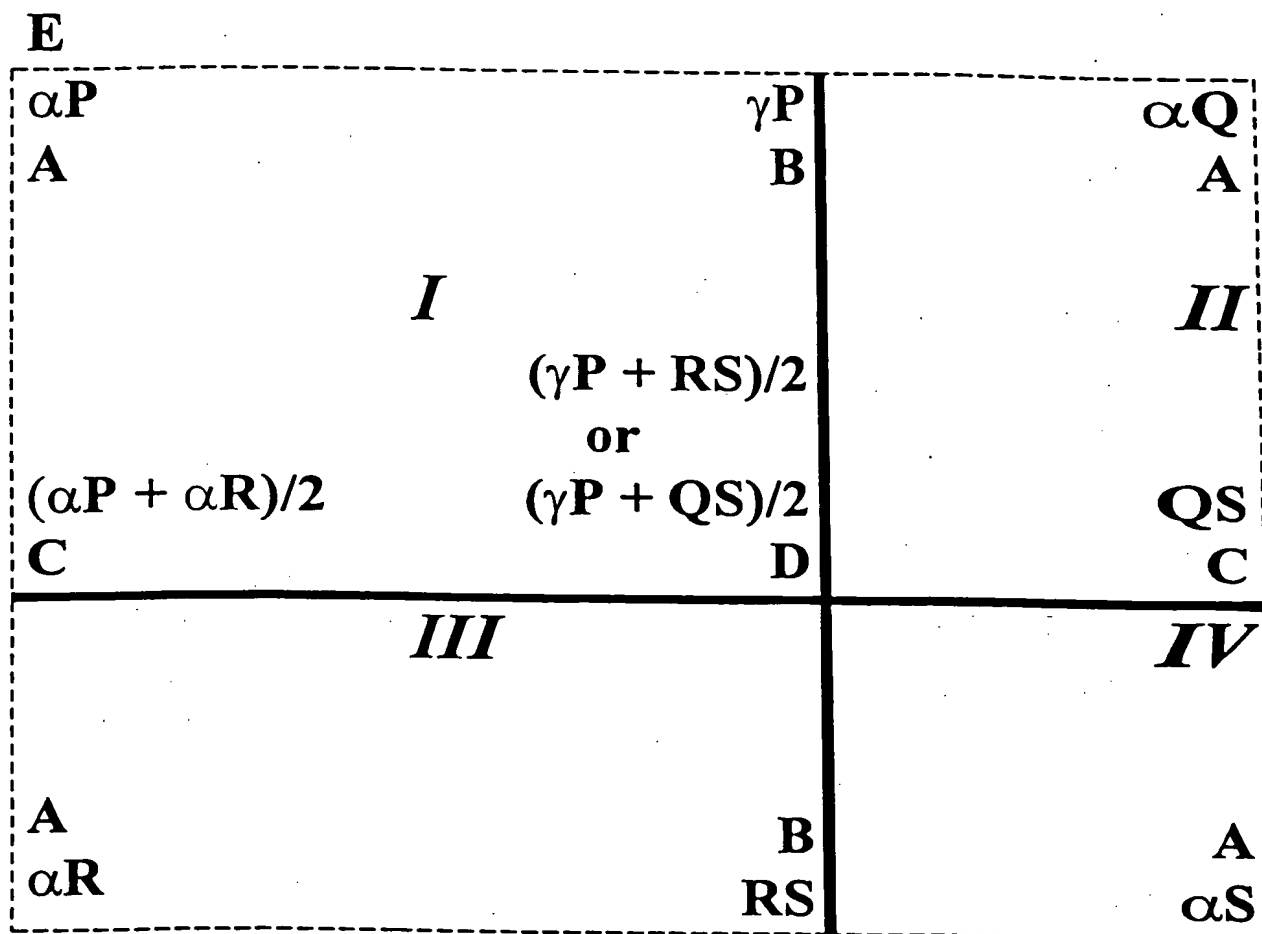
400 ppi pixel F



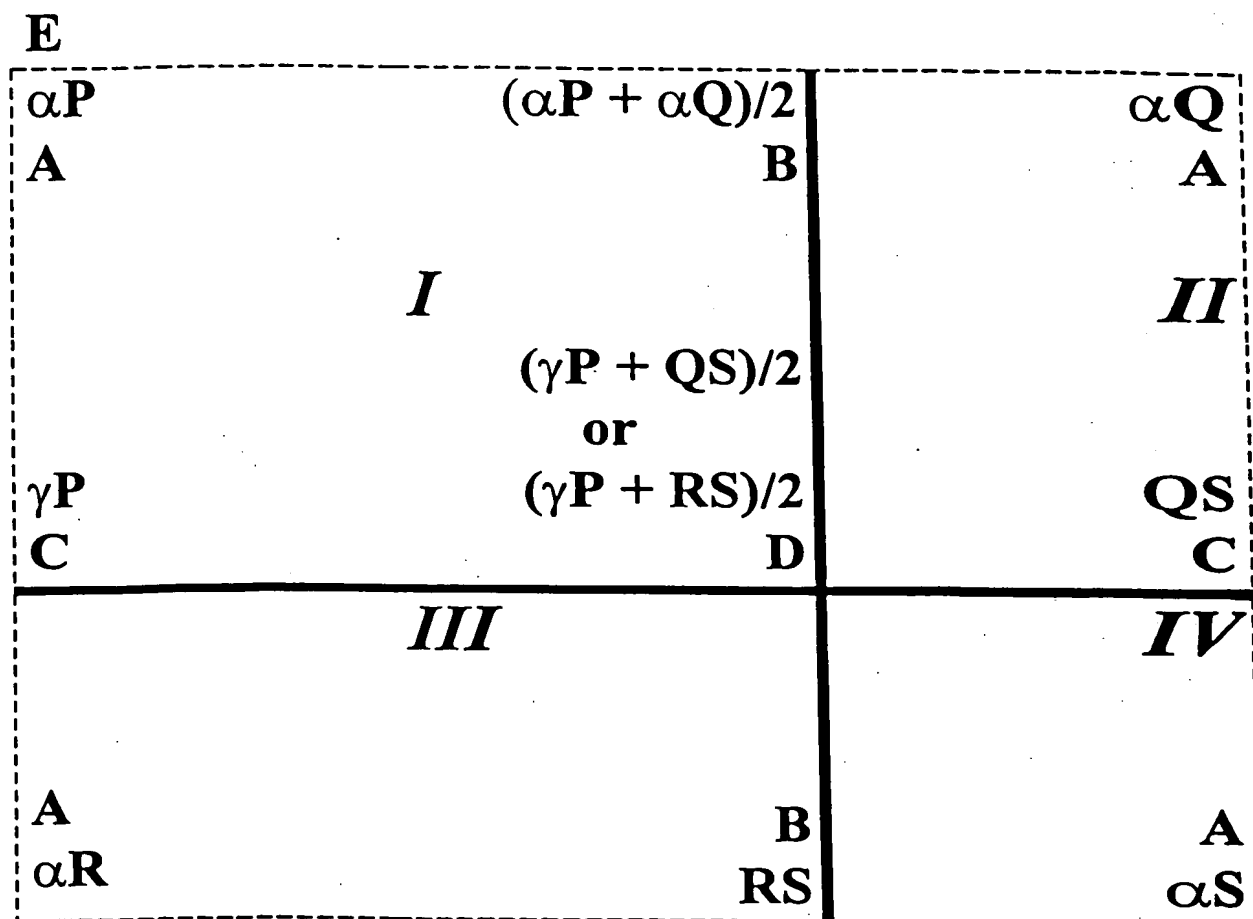
400 ppi pixel G

400 ppi pixel H

Figure 1



**FIGURE 2**



**FIGURE 3**

LSTPTR-2	LSTPTR BYTE U'		LSTPTR+2
SRCPTR-2 BYTE V'	SRCPTR BYTE P'		SRCPTR+2 BYTE Q'
	A	B	
	C	QUAD. I D	QUAD. II
NXTPTR-2	NXTPTR BYTE R'		NXTPTR+2 BYTE S'
	QUAD. III		QUAD. IV

FIGURE 4

**QUADRANT I**  
**E**

**QUADRANT II**

<b>SRCPTR</b>  <b>X</b>  <b>A</b>	  <b>X</b>  <b>B</b>	<b>SRCPTR+2</b>  <b>X</b>
<b>OFFSET PTR</b>  <b>X</b>  <b>C</b>	  <b>X</b>  <b>D</b>	  <b>X</b>
  <b>X</b>  <b>NXTPTR</b>	  <b>X</b>	  <b>X</b>  <b>NXTPTR+2</b>

**QUADRANT III**

**QUADRANT IV**

**FIGURE 5**

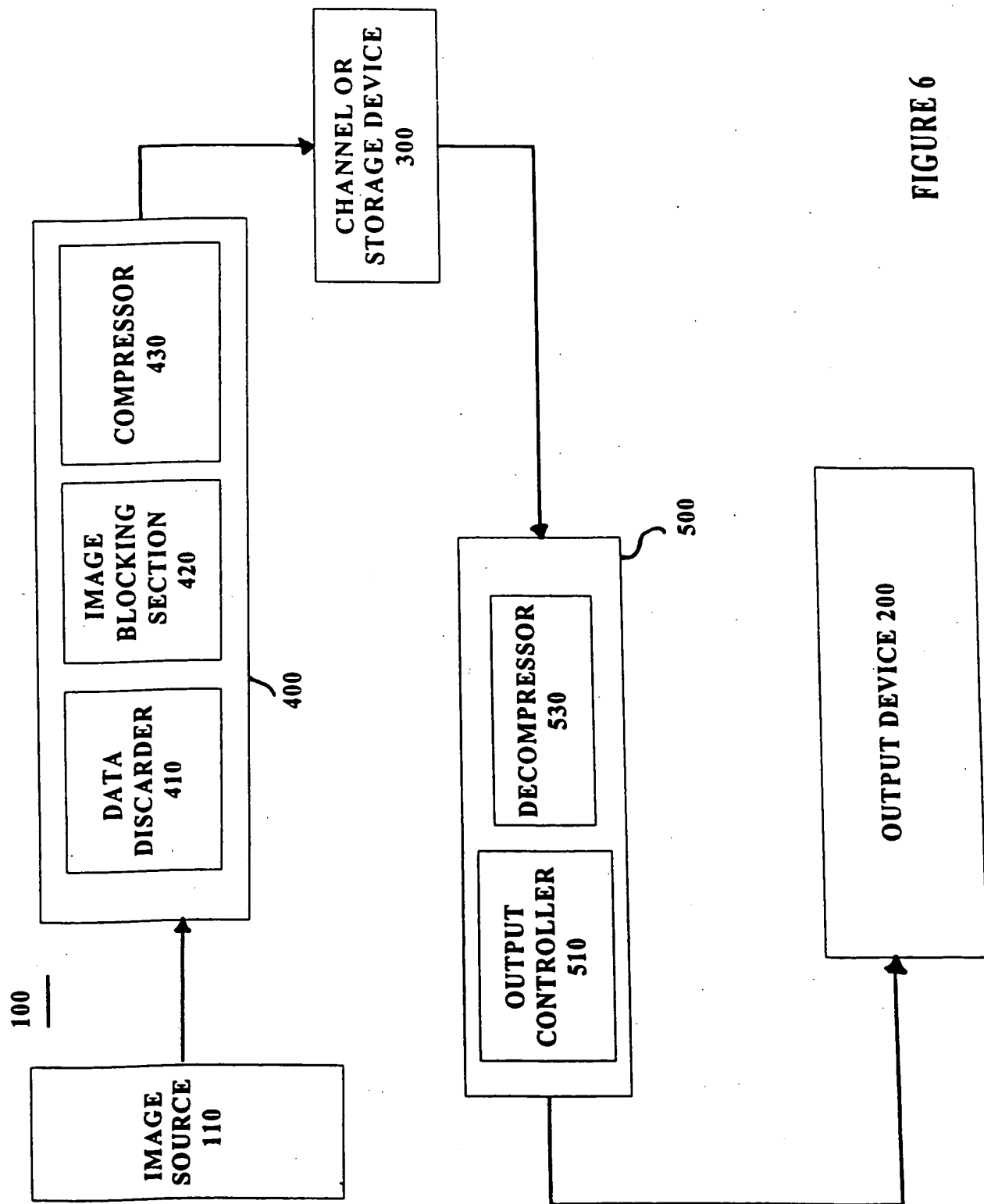


FIGURE 6

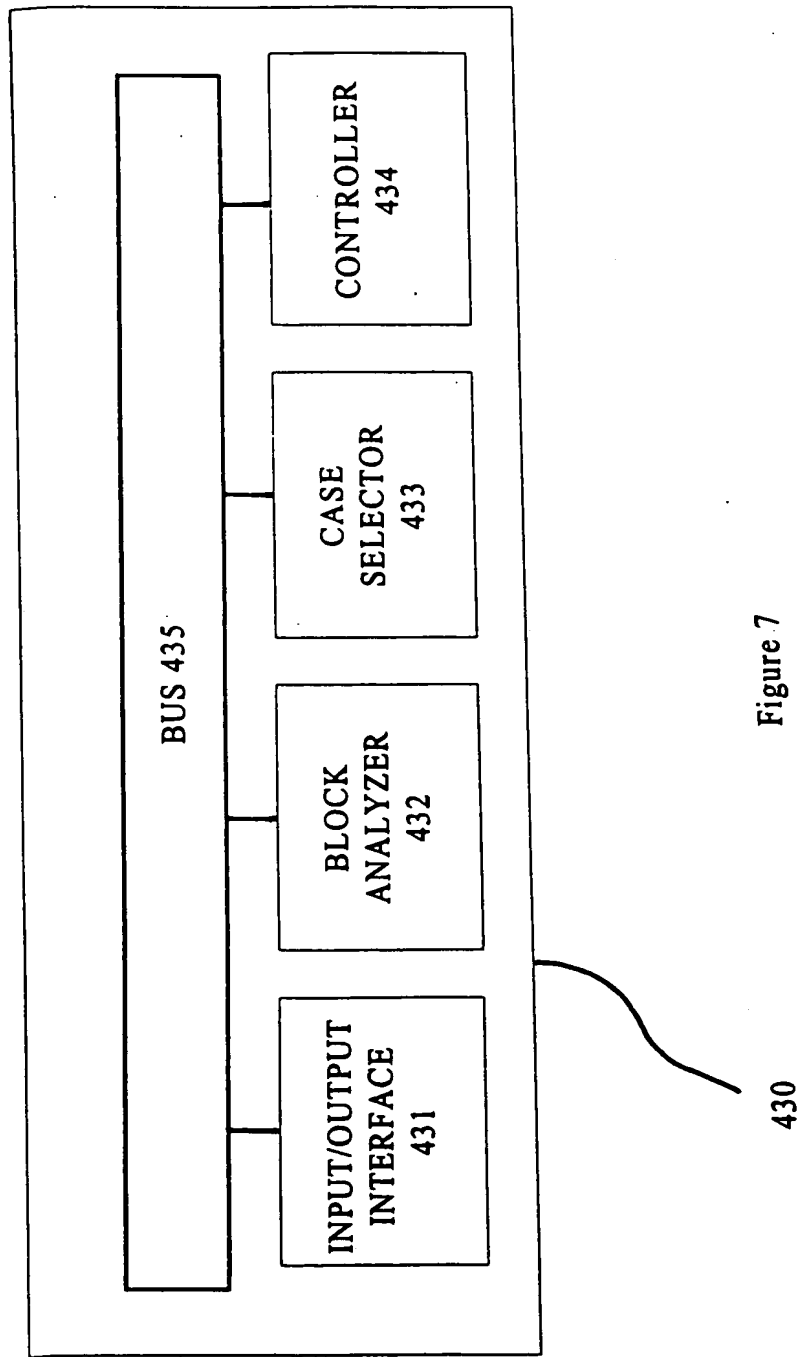


Figure 7

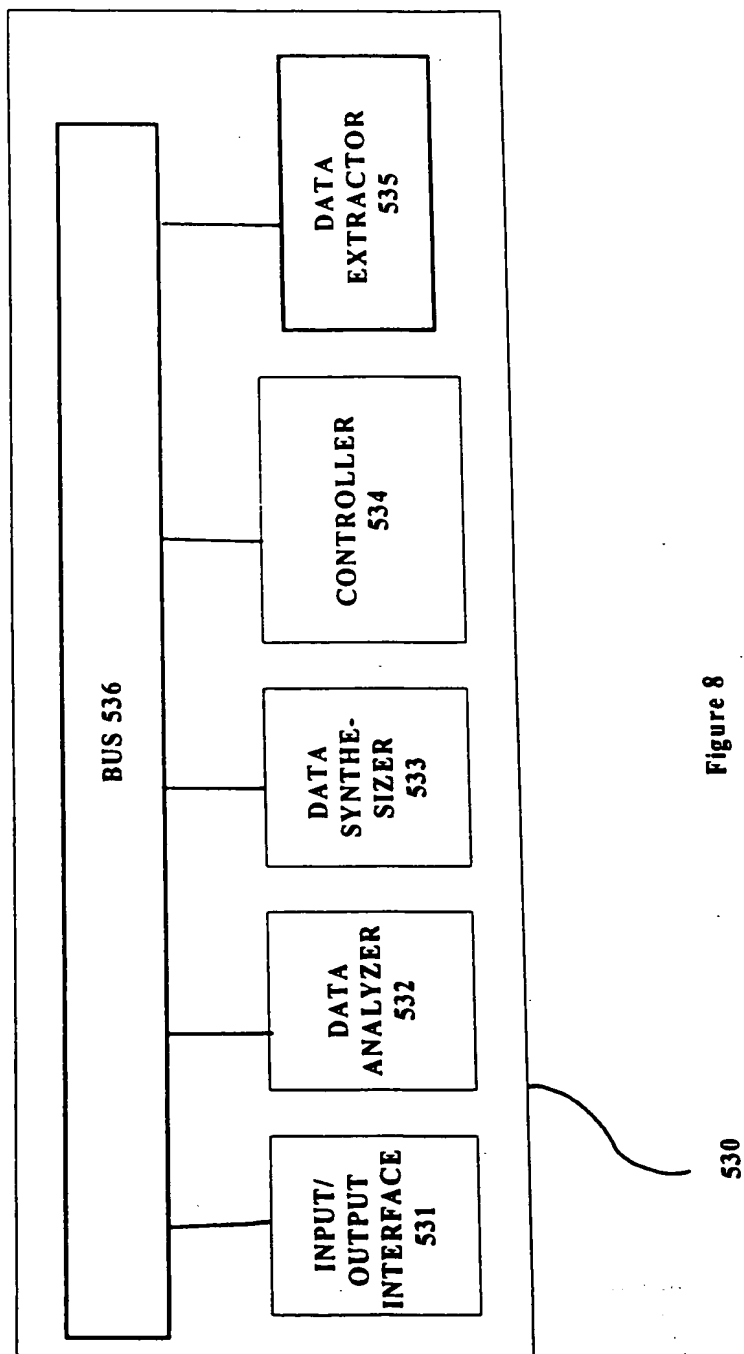


Figure 8



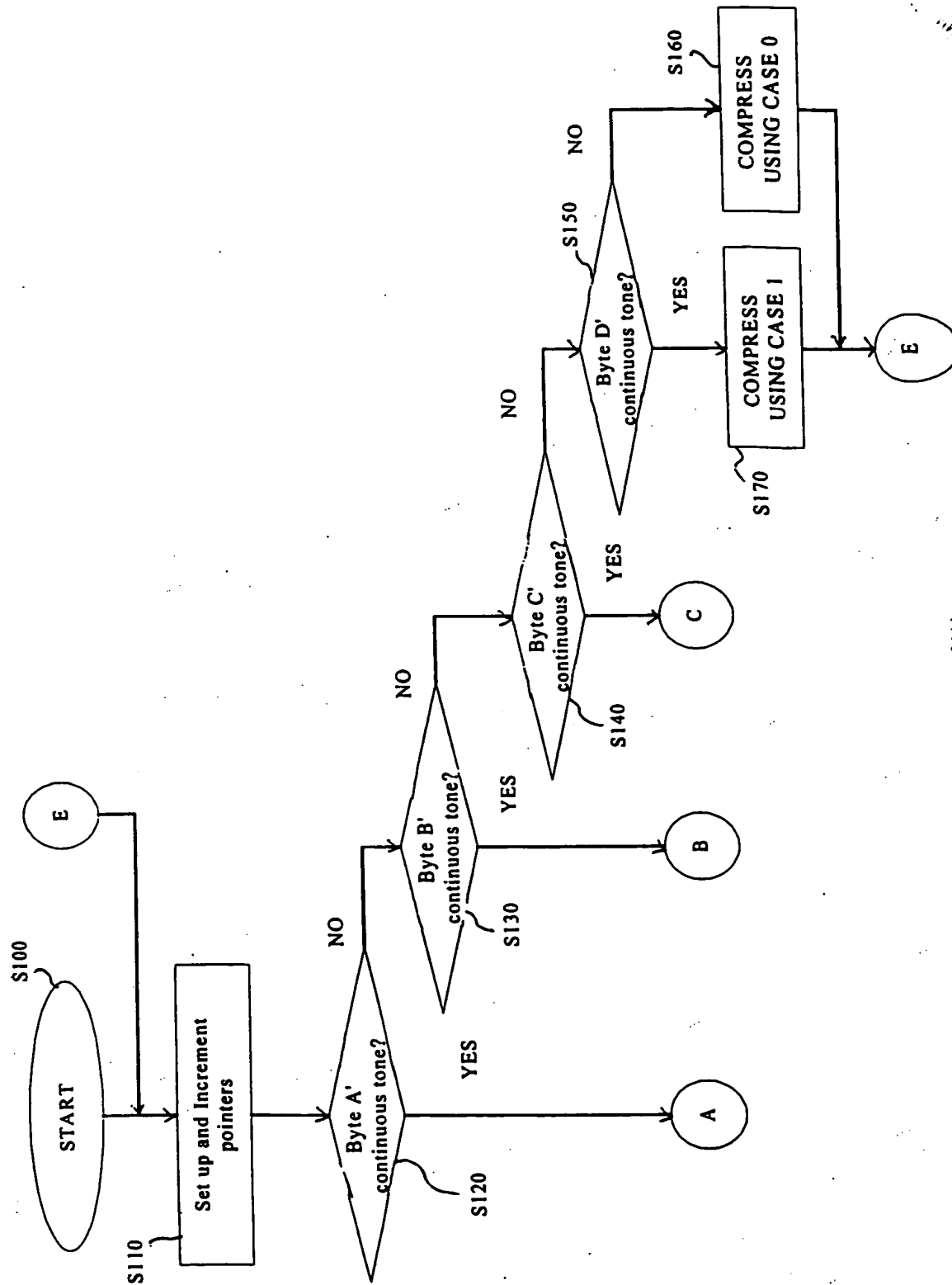


Figure 9(A)

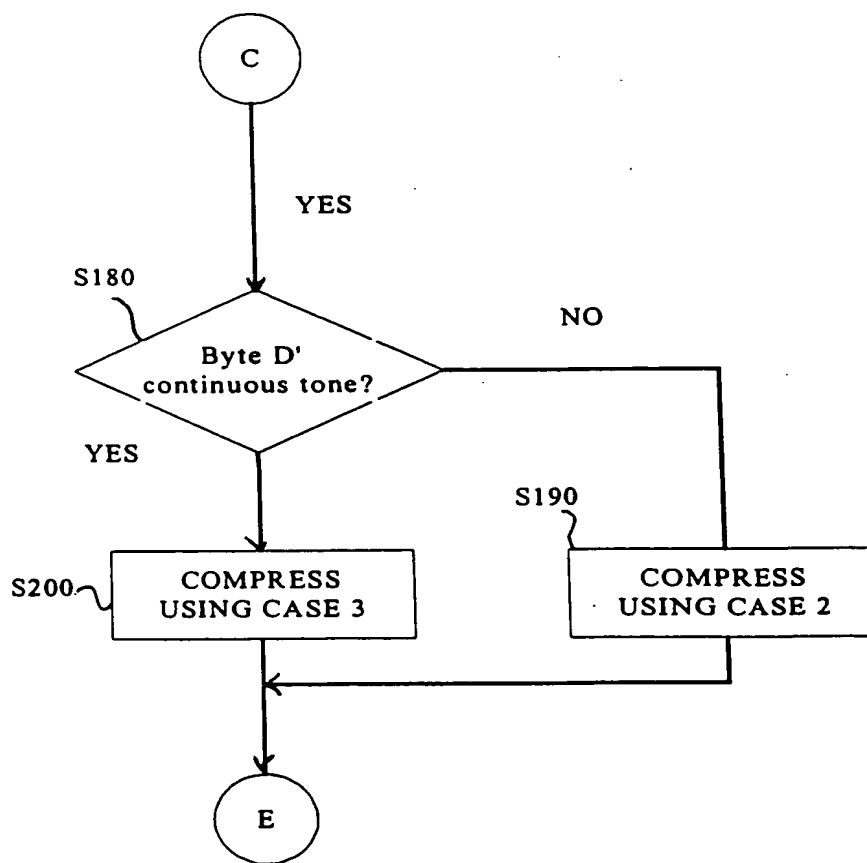


Figure 9(B)

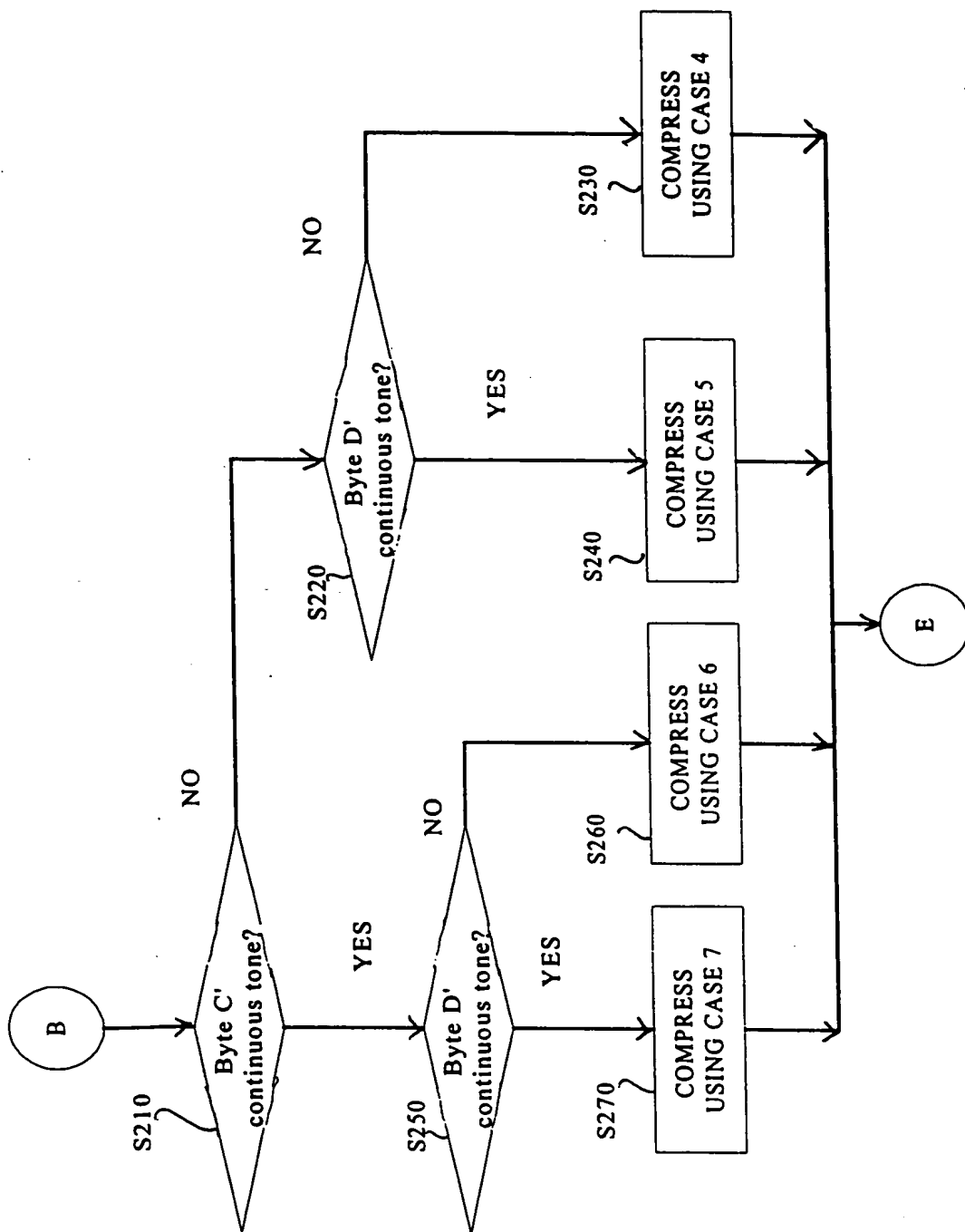


Figure 9(C)

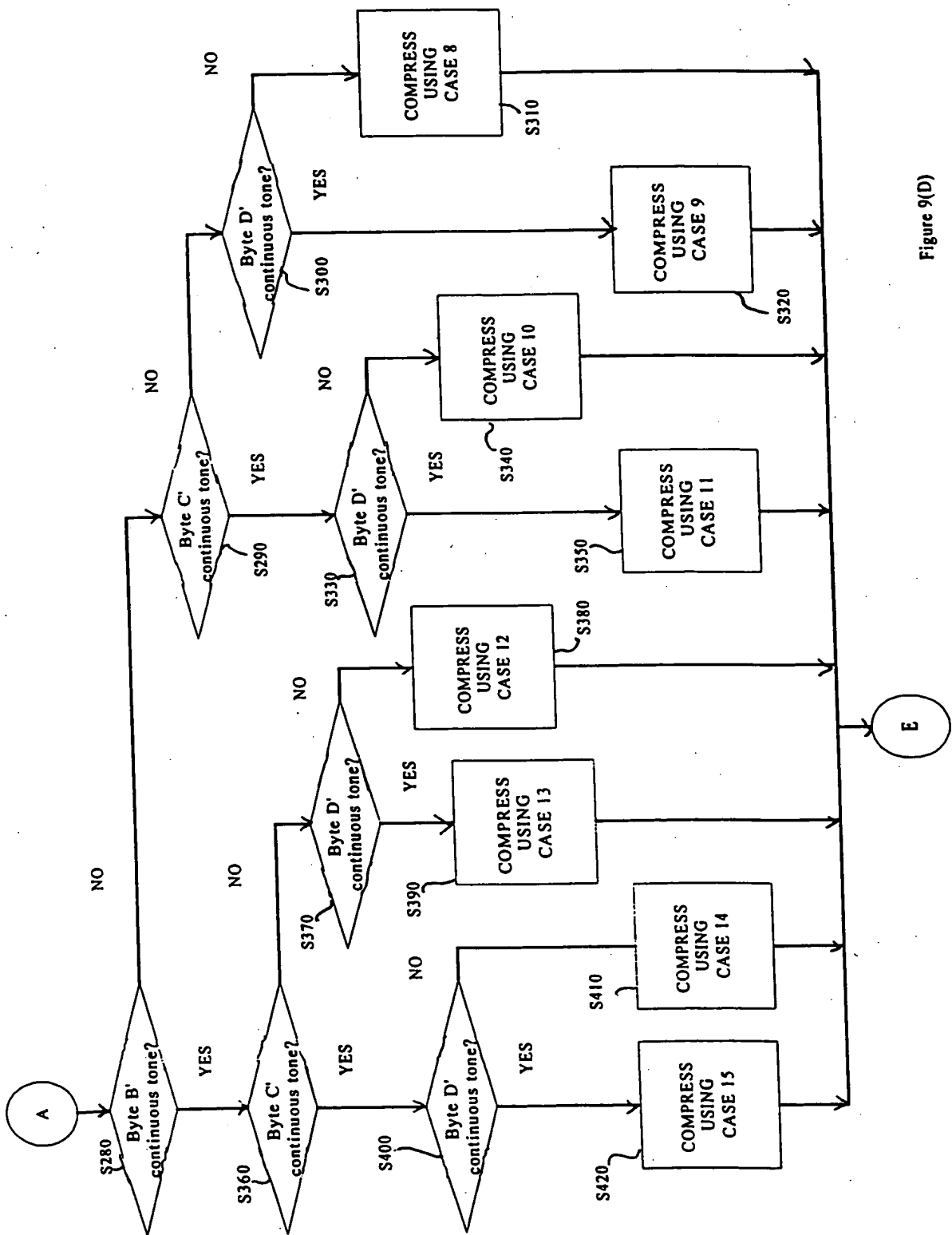
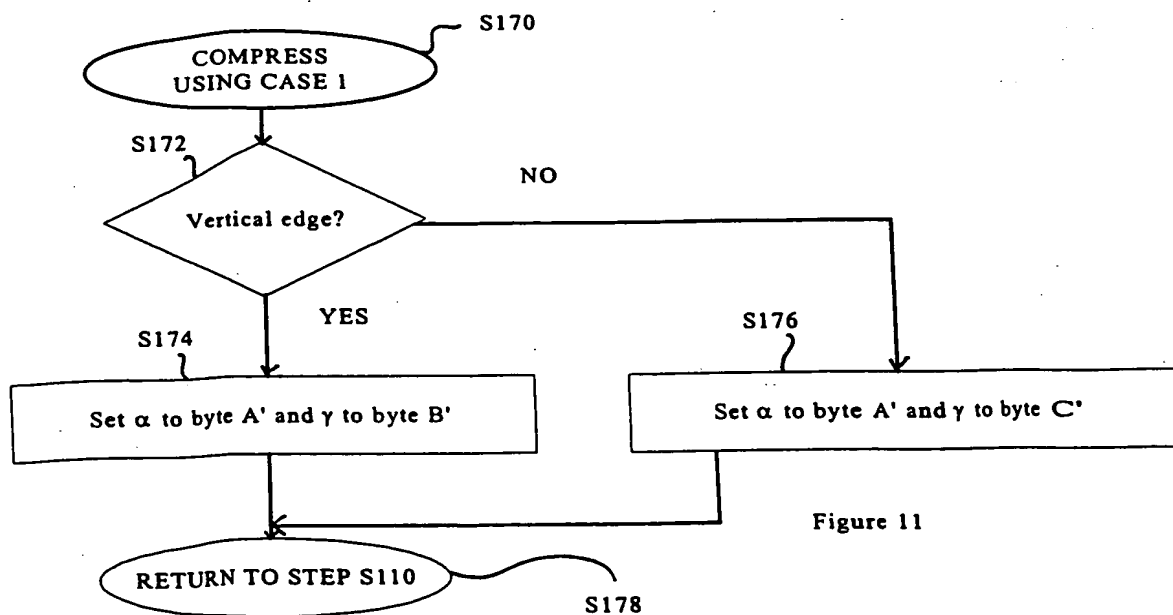
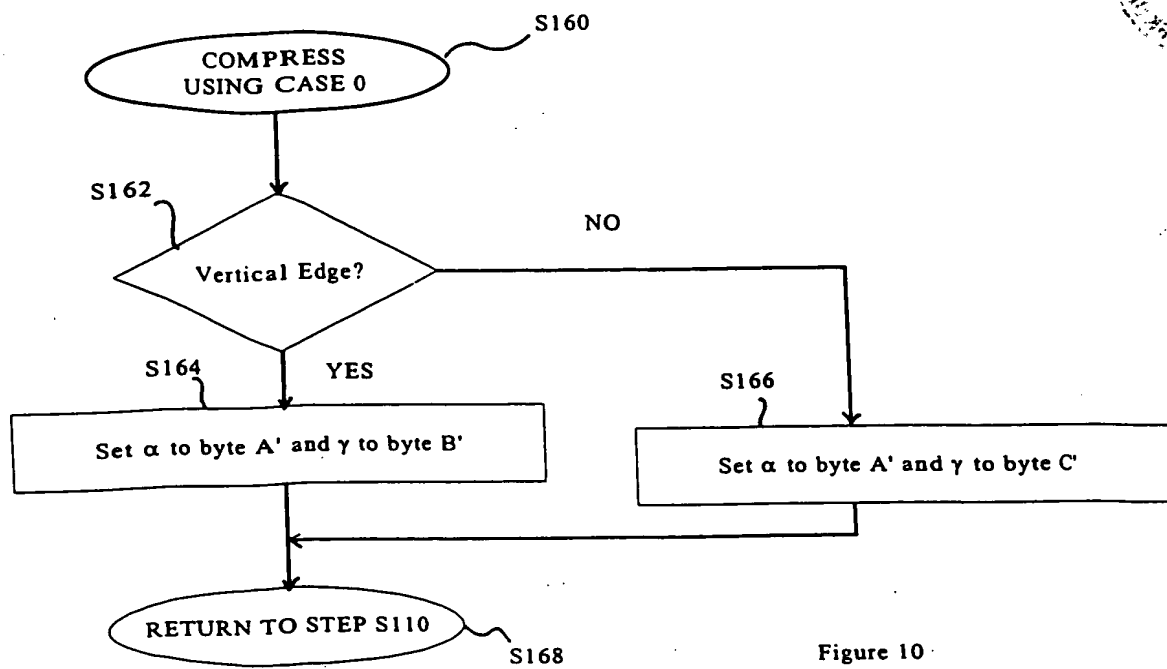
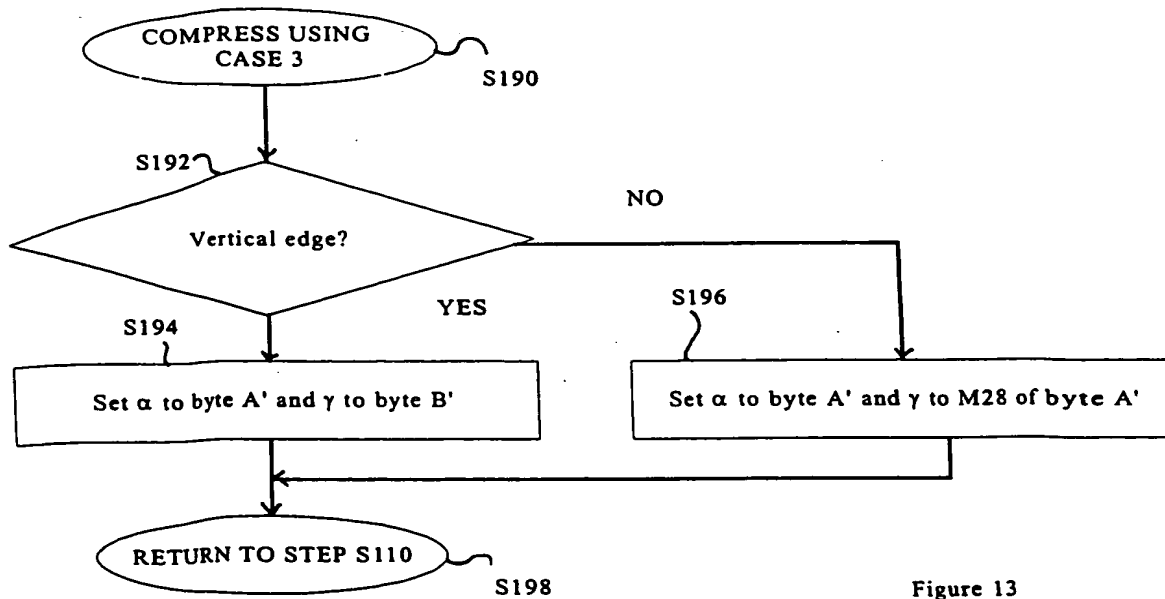
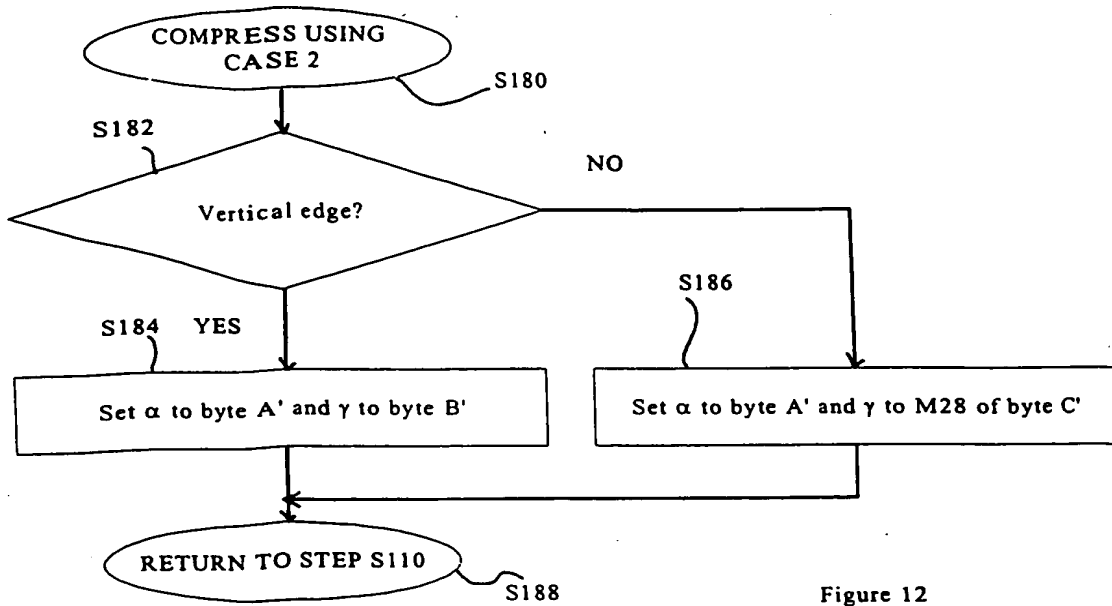
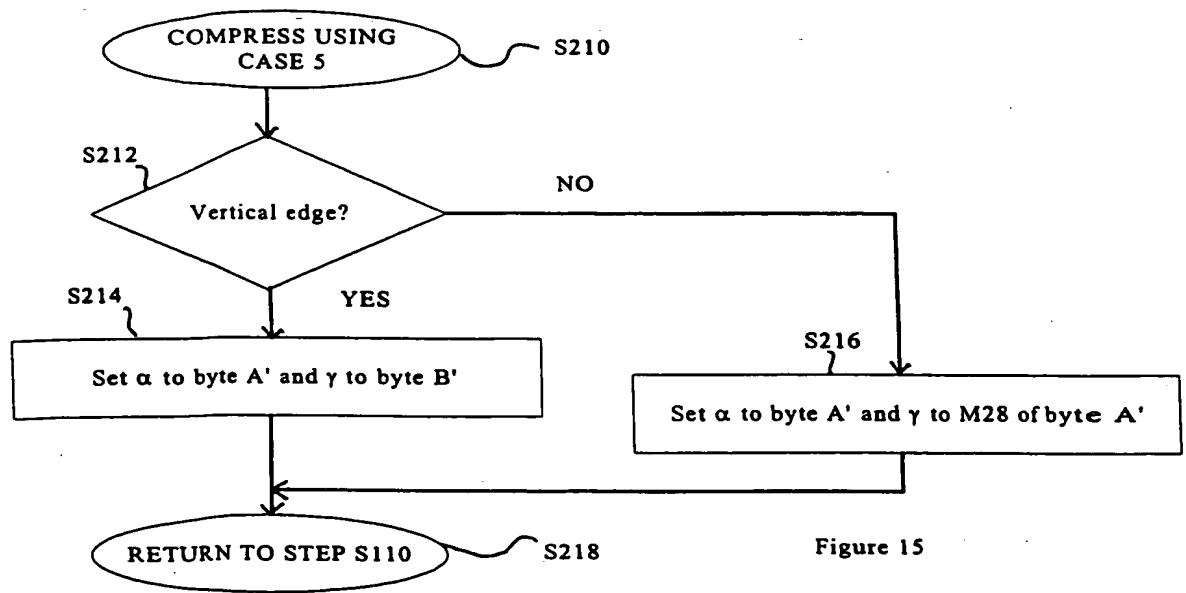
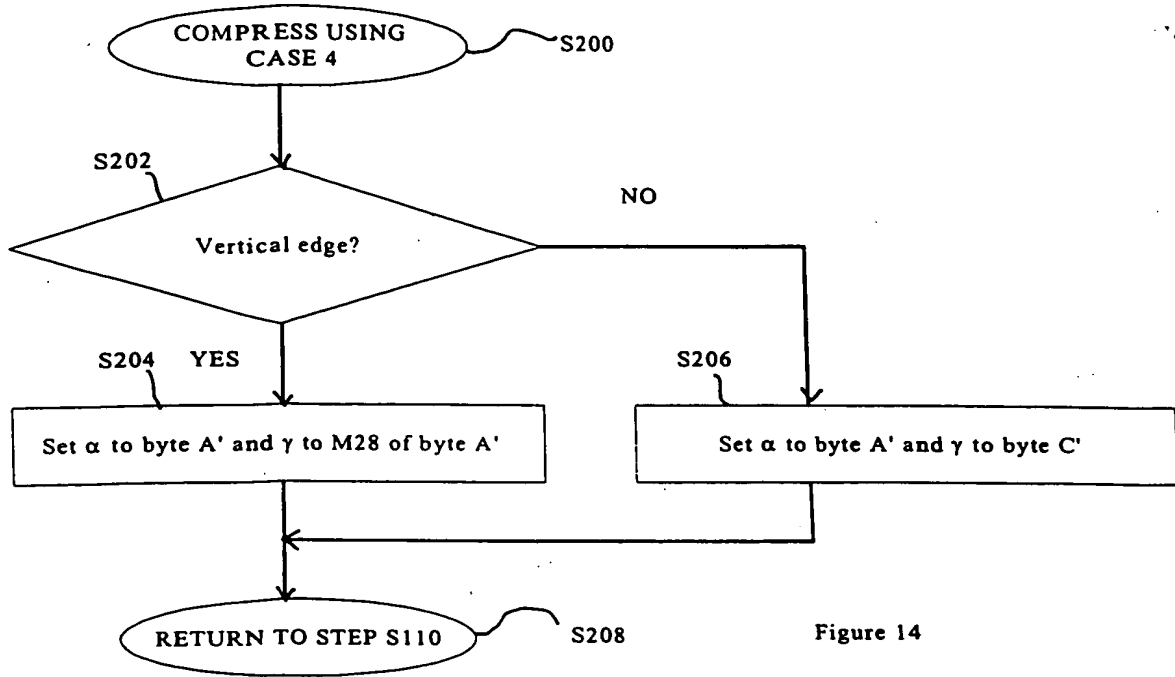


Figure 9(D)







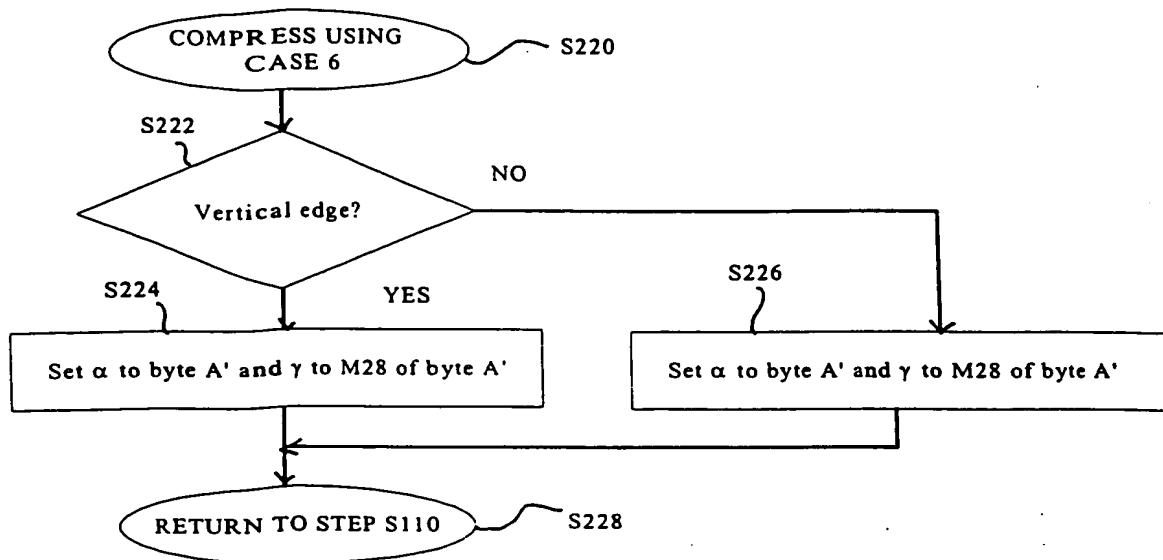


Figure 16

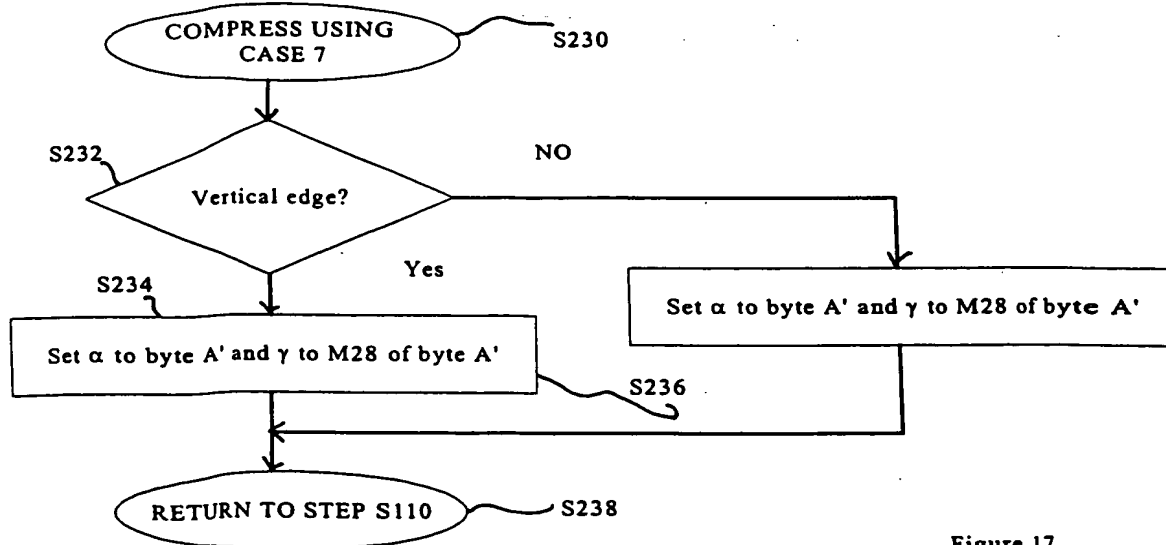
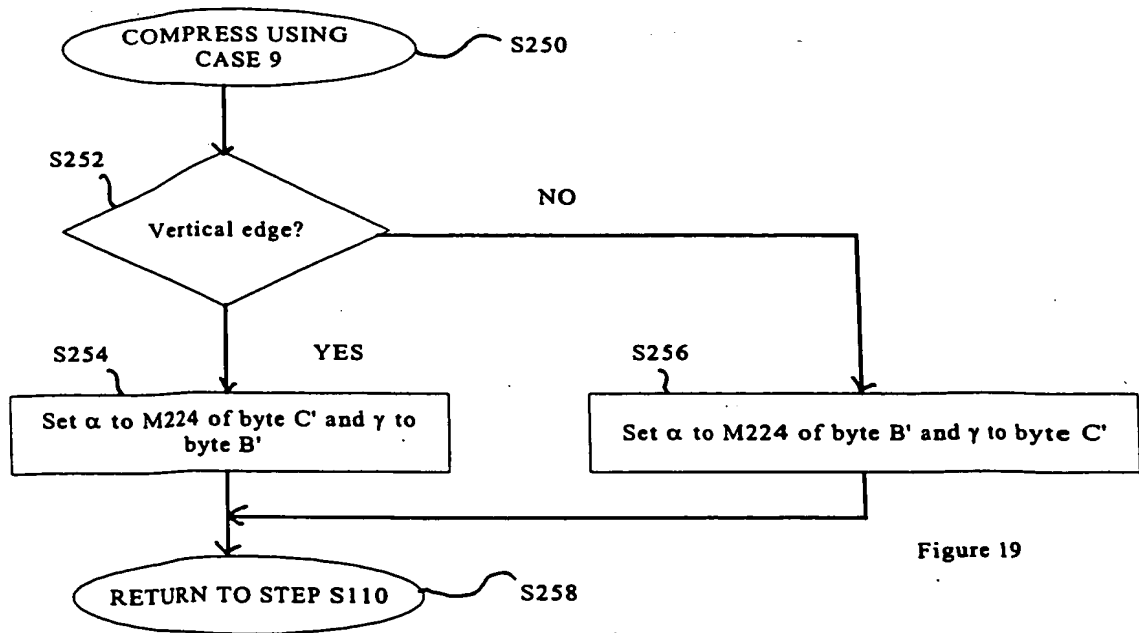
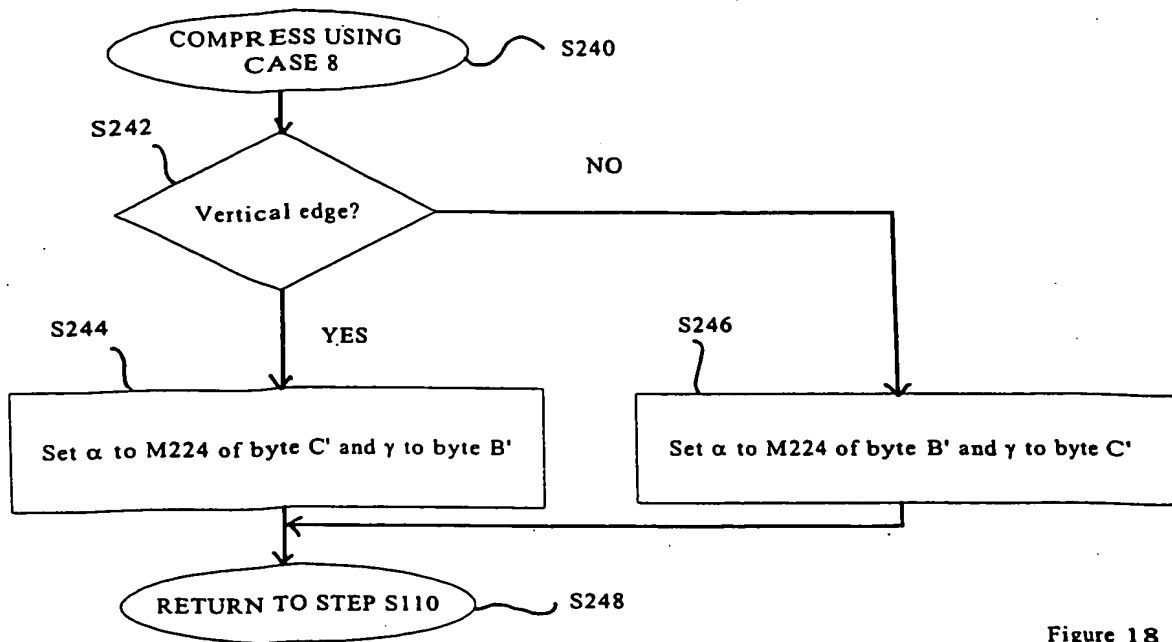


Figure 17





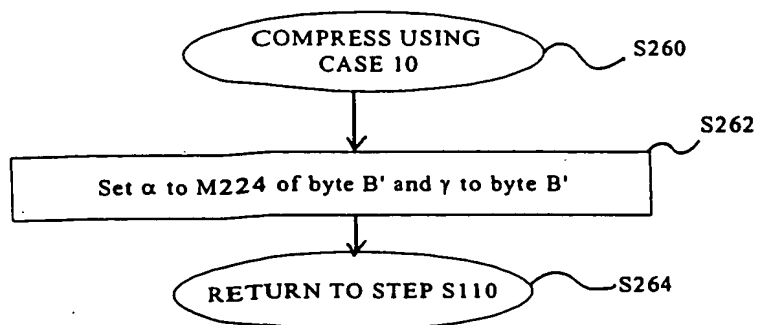


Figure 20

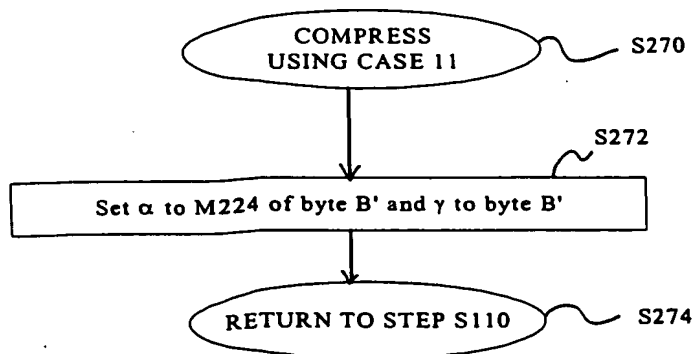


Figure 21

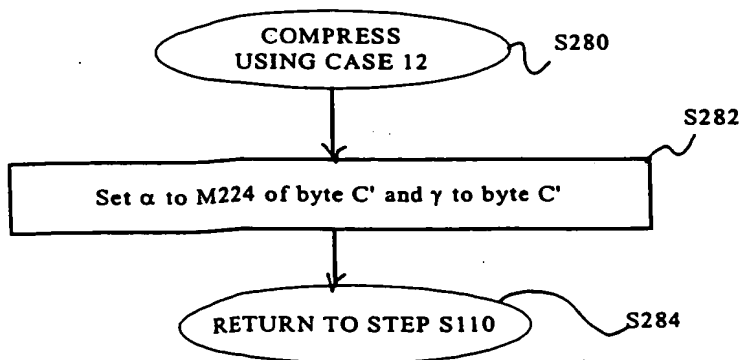


Figure 22

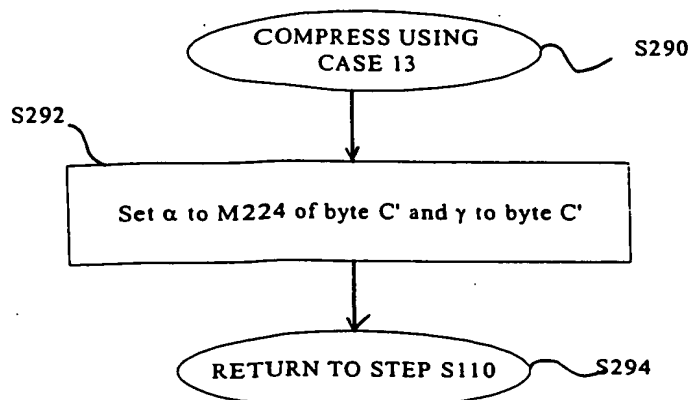


Figure 23

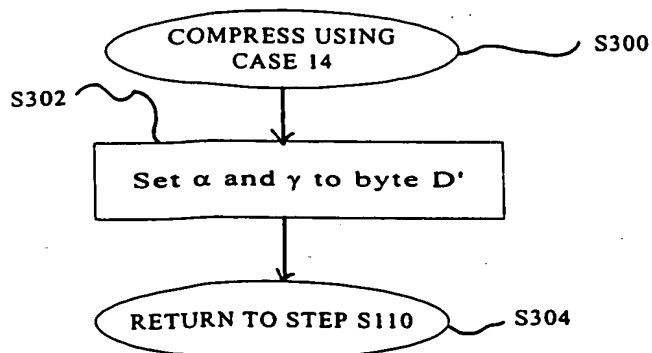


Figure 24

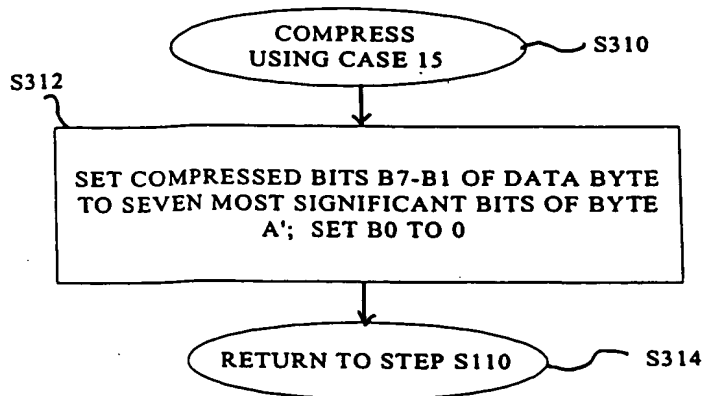
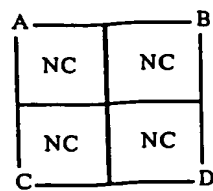
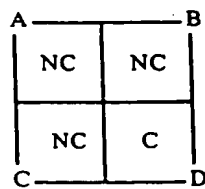


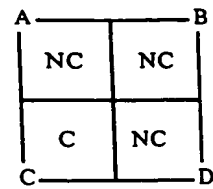
Figure 25



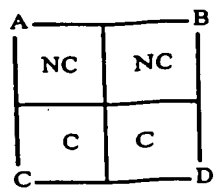
CASE 0



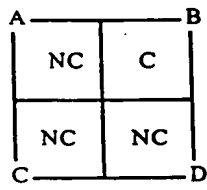
CASE 1



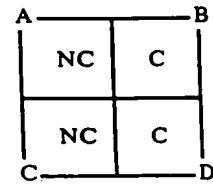
CASE 2



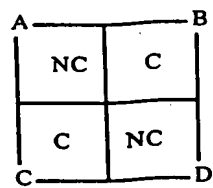
CASE 3



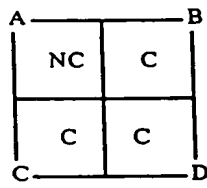
CASE 4



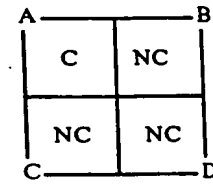
CASE 5



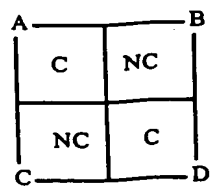
CASE 6



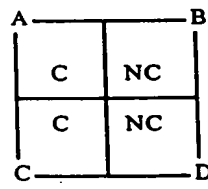
CASE 7



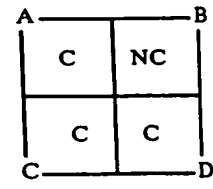
CASE 8



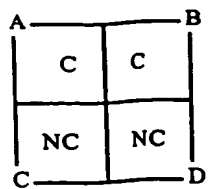
CASE 9



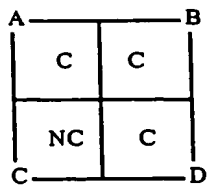
CASE 10



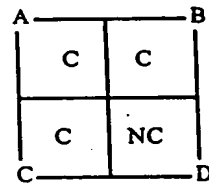
CASE 11



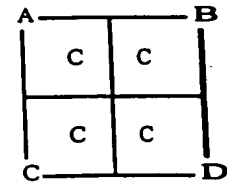
CASE 12



CASE 13



CASE 14



CASE 15

Figure 26

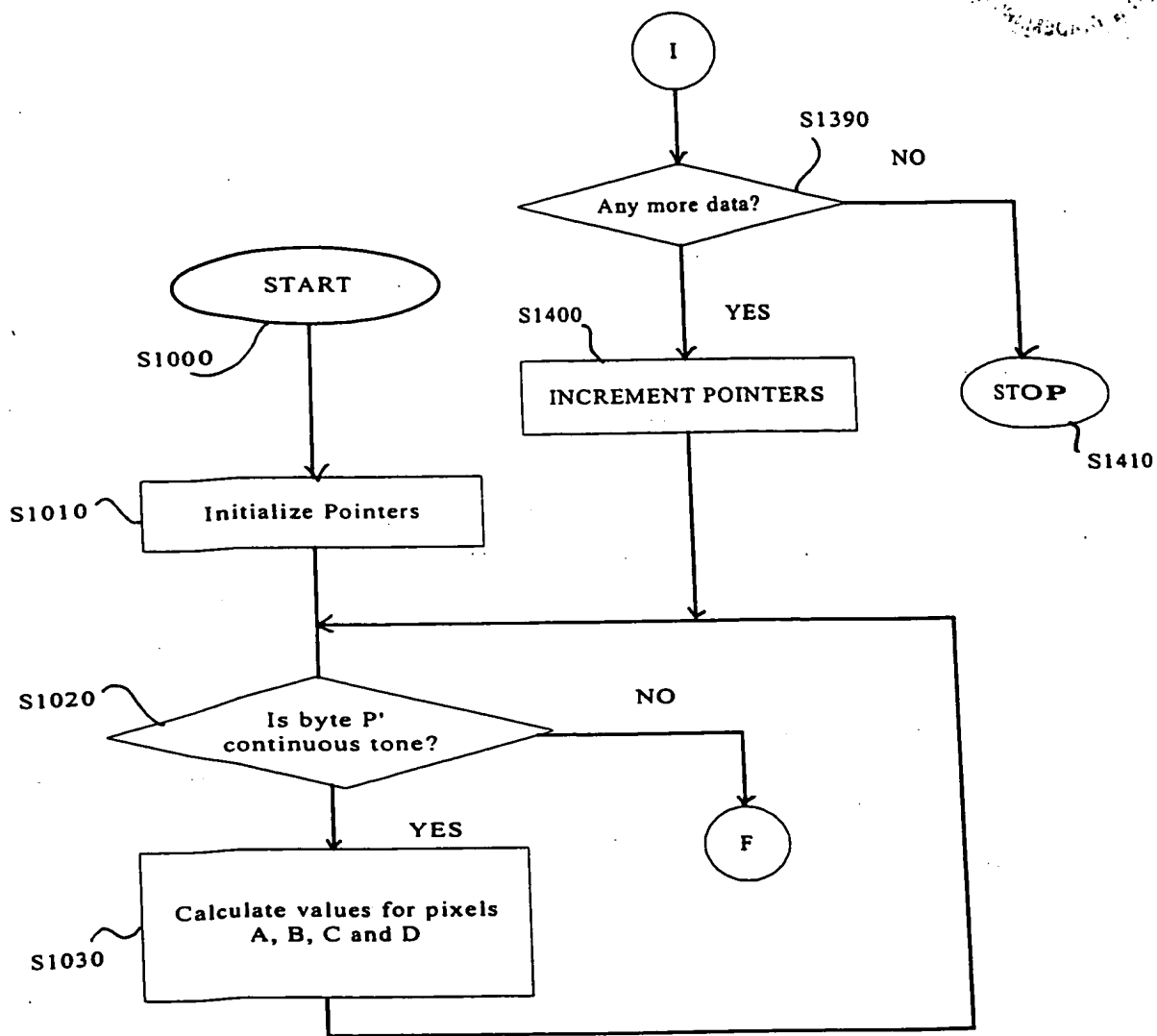


Figure 27(A)

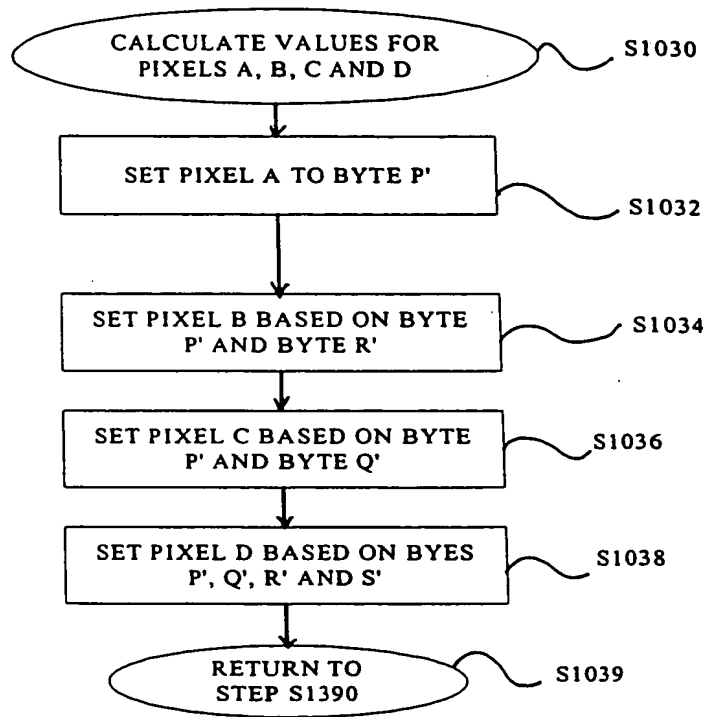


FIGURE 27(B)

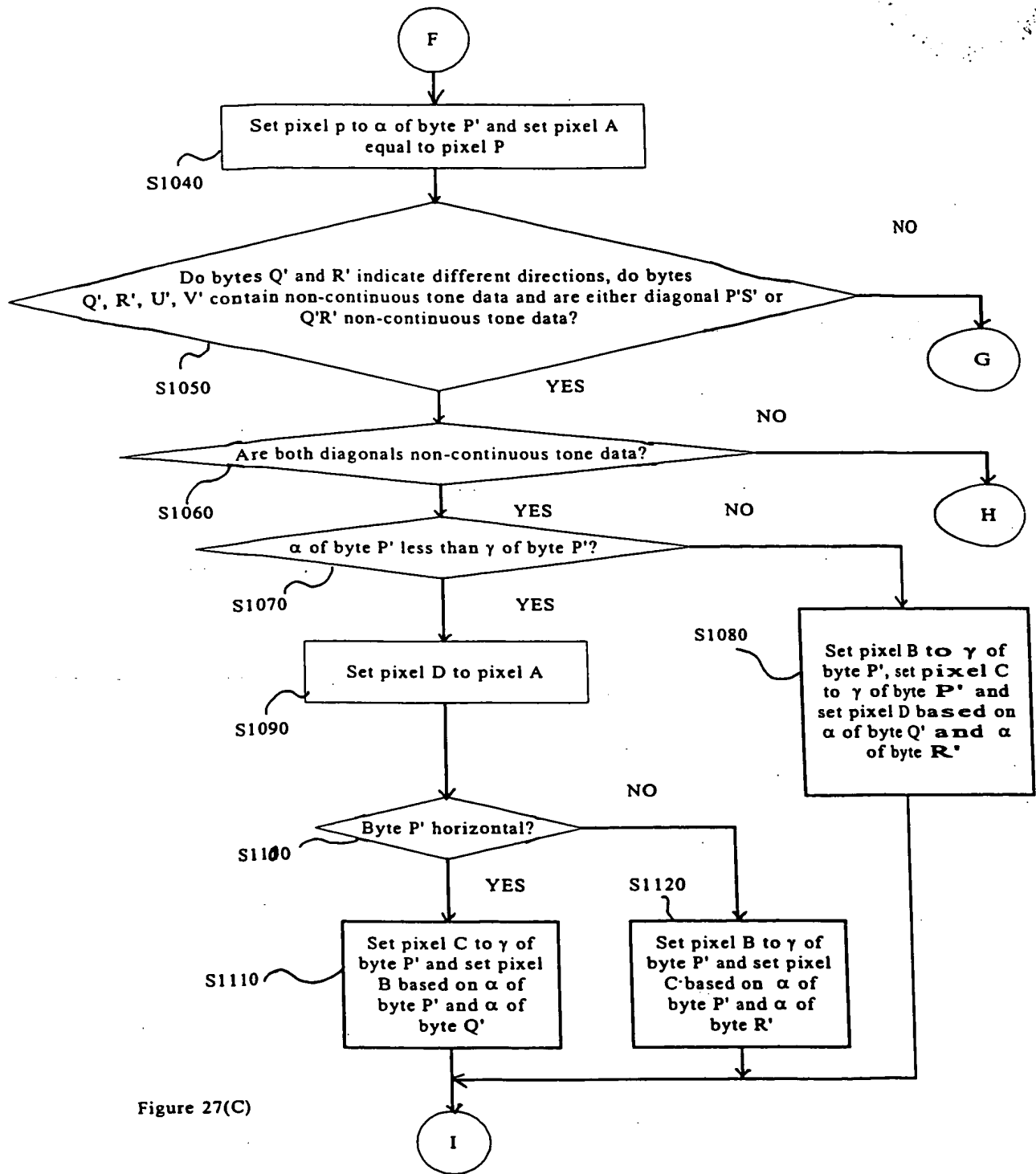


Figure 27(C)

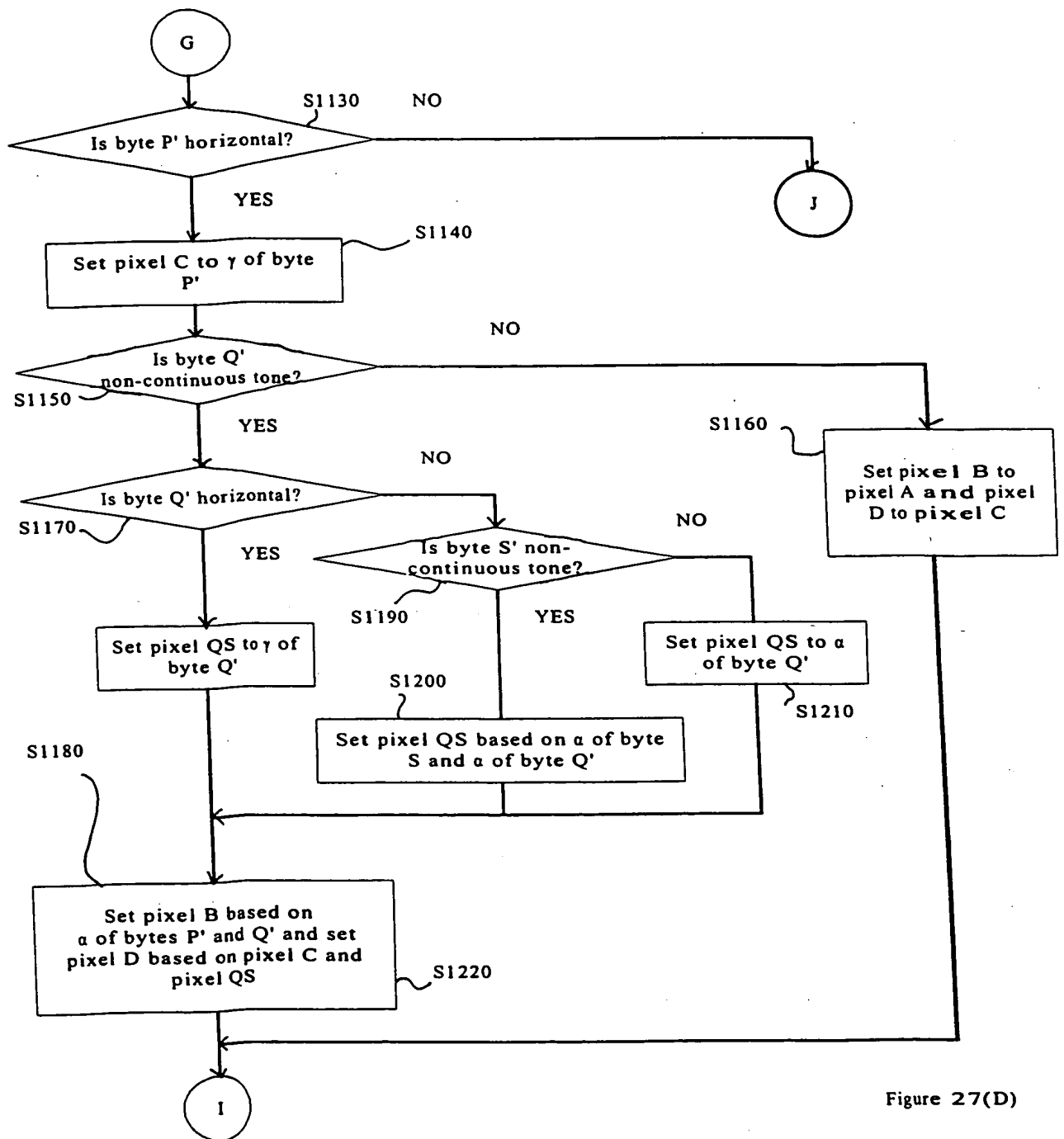


Figure 27(D)



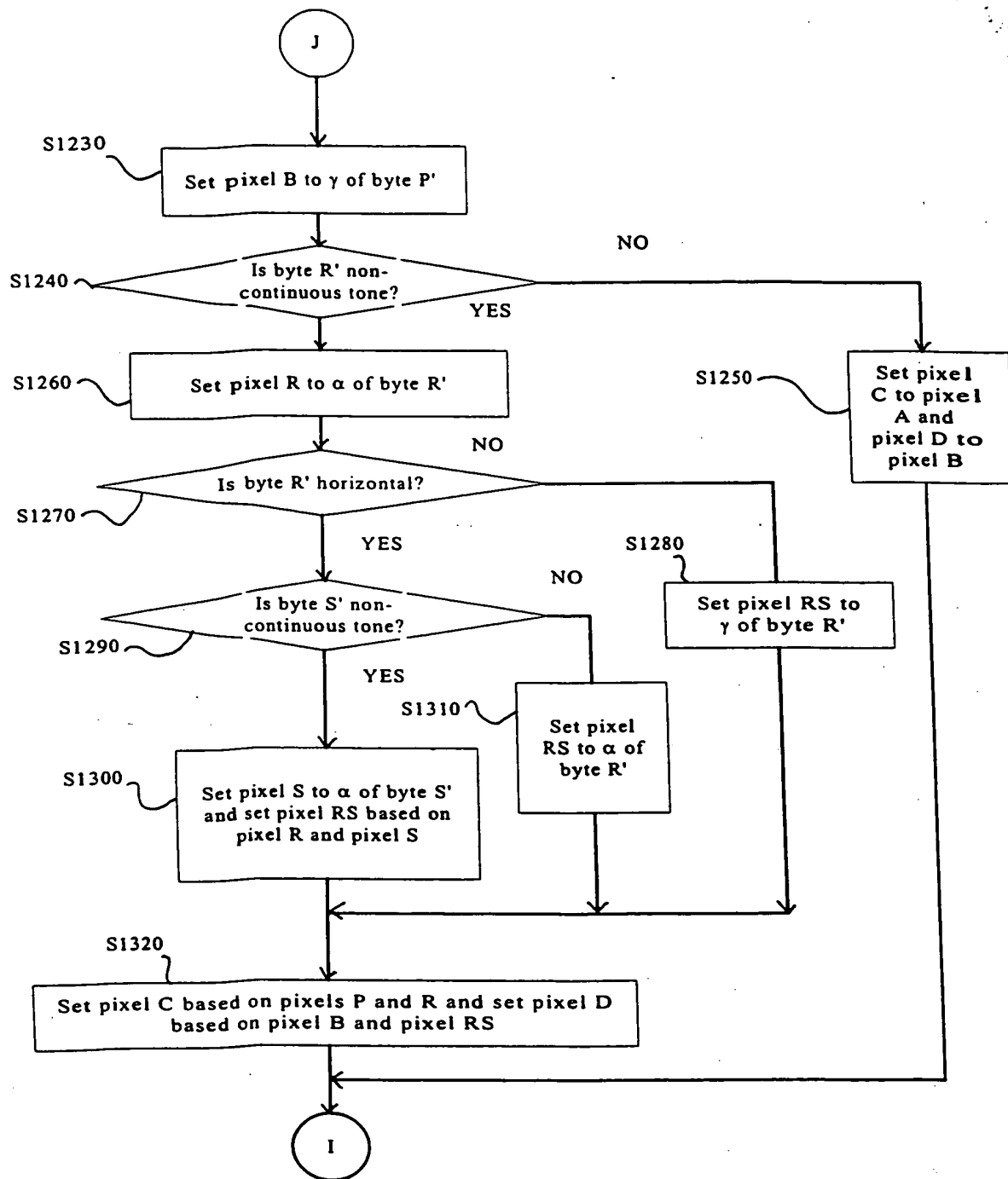


Figure 27(E)

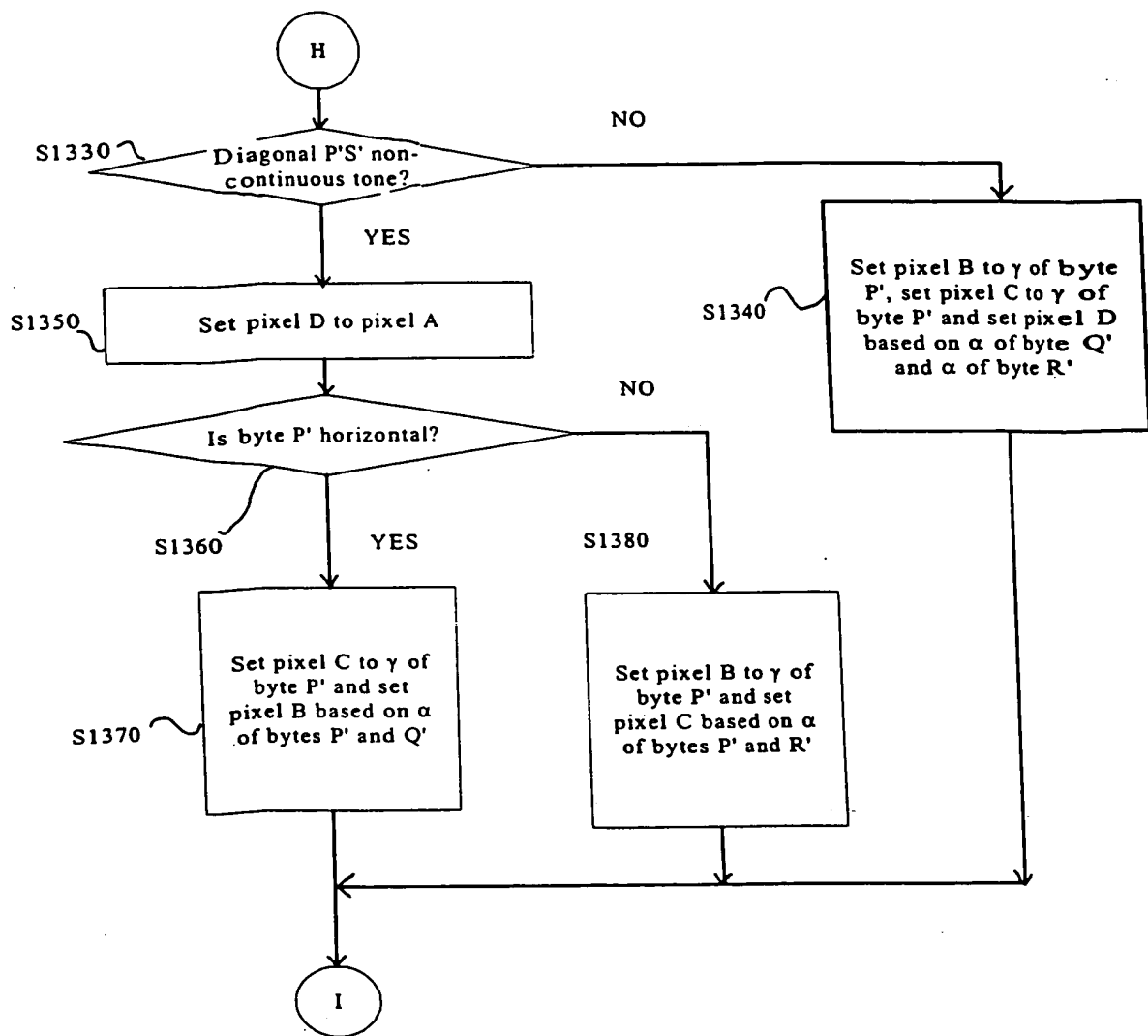


Figure 27(F)

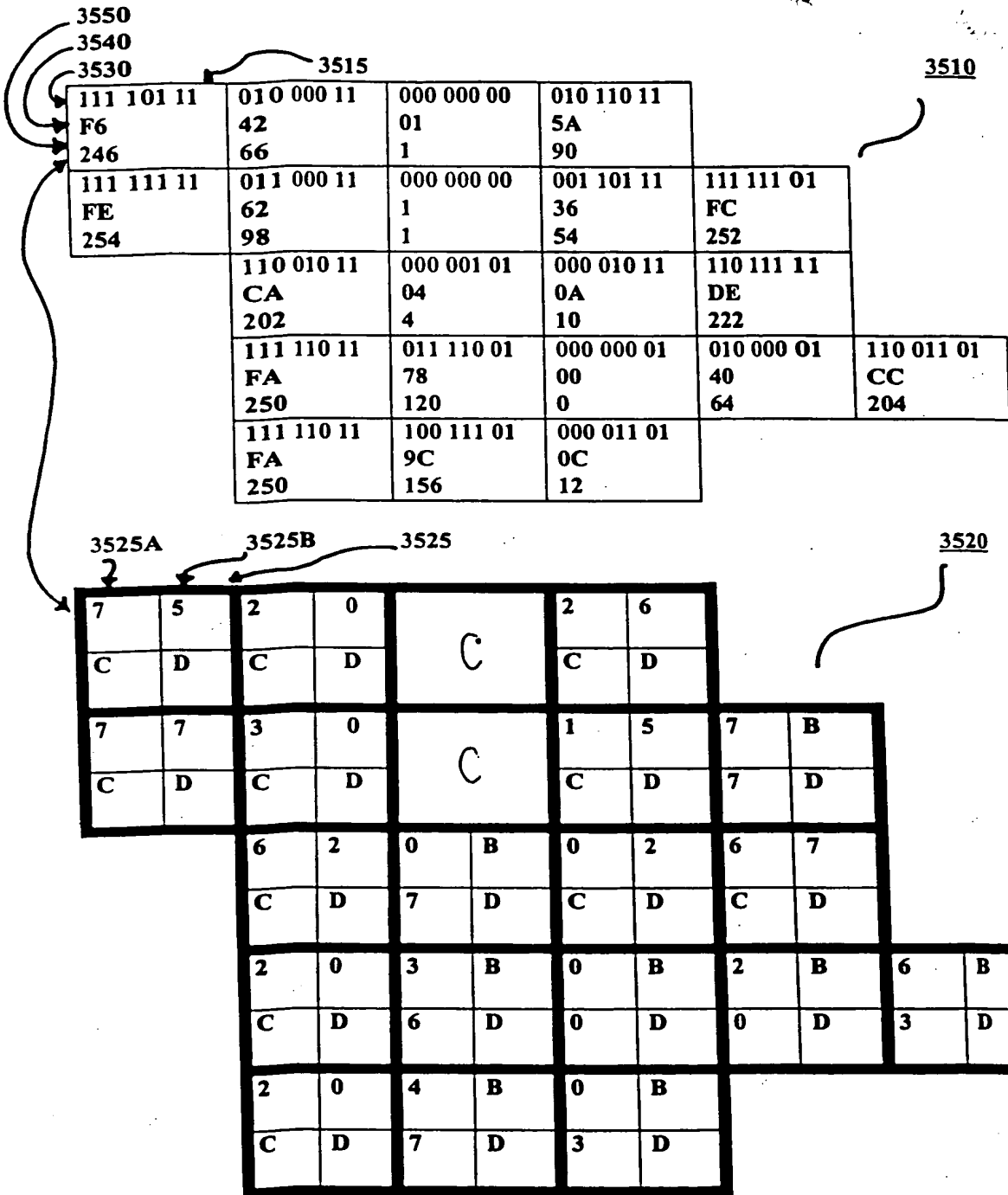


FIGURE 28